



DPBT8105

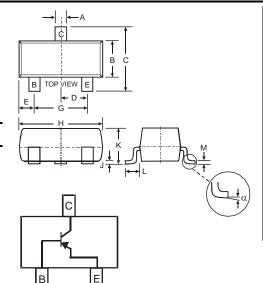
1A PNP SURFACE MOUNT TRANSISTOR

Features

- **Epitaxial Planar Die Construction**
- Ideal for Medium Power Amplification and Switching
- High Collector Current Rating
- Complementary Version Available (DNBT8105)
- Lead, Halogen and Antimony Free, RoHS Compliant "Green" Device (Notes 2, 3 and 4)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT-23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking Information: K82, See Page 3
- Ordering & Date Code Information: See Page 3
- Weight: 0.008 grams (approximate)



SOT-23								
Dim	Min	Max						
Α	0.37	0.51						
В	1.20	1.40						
С	2.30	2.50						
D	0.89	1.03						
E	0.45	0.60						
G	1.78	2.05						
Н	2.80	3.00						
J	0.013	0.10						
K	0.903	1.10						
L	0.45	0.61						
M	0.085	0.180						
α	0°	8°						
All Dimensions in mm								

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit	
Collector-Base Voltage	V _{CBO}	-80	V	
Collector-Emitter Voltage	V _{CEO}	-60	V	
Emitter-Base Voltage	V _{EBO}	-5	V	
Collector Current - Continuous	Ic	-1	Α	
Peak Pulse Collector Current	I _{CM}	-2	Α	

Top View

Thermal Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit	
Power Dissipation (Note 1)	P _D	600	mW	
Thermal Resistance, Junction to Ambient (Note 1)	$R_{ hetaJA}$	209	°C/W	
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C	

Notes:

- 1. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- No purposefully added lead. Halogen and Antimony Free.
- Diode's Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
- Product is manufactured with Green Molding Compound and does not contain Halogens or Sb₂O₃ Fire Retardants.



Electrical Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)	Syllibol	IVIIII	IVIAX	Onit	rest Condition
Collector-Base Breakdown Voltage	V _{(BR)CBO}	-80	_	V	I _C = -100μA, I _E = 0
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	-60	_	V	I _C = -10mA, I _B = 0
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	-5	_	V	I _E = -100μA, I _C = 0
Collector Cutoff Current	Ісво	_	-100	nA	V _{CB} = -60V, I _E = 0
Collector Cutoff Current	I _{CES}	_	-100	nA	V _{CES} = -60V
Emitter Cutoff Current	I _{EBO}		-100	nA	$V_{EB} = -4V, I_C = 0$
ON CHARACTERISTICS (Note 5)					
	h _{FE}	100	_		$I_C = -1 \text{mA}$, $V_{CE} = -5 \text{V}$
DC Current Gain		100	300	V	$I_C = -500 \text{mA}, V_{CE} = -5 \text{V}$
Do Current Gain		80	_		$I_C = -1A$, $V_{CE} = -5V$
		30			$I_C = -2A$, $V_{CE} = -5V$
Collector Emitter Coturation Voltage	V _{CE(SAT)}	_	-0.3	V	$I_C = -500 \text{mA}, I_B = -50 \text{mA}$
Collector-Emitter Saturation Voltage		_	-0.6	V	$I_C = -1A$, $I_B = -100mA$
Base-Emitter Saturation Voltage	V _{BE(SAT)}	_	-1.2	V	I _C = -1A, I _B = -100mA
Base-Emitter Turn On Voltage	V _{BE(ON)}	_	-1.0	V	$I_{C} = -1A, V_{CE} = -5V$
SMALL SIGNAL CHARACTERISTICS					
Output Capacitance	C _{obo}	_	12	pF	V _{CB} = -10V, f = 1.0MHz
Current Gain-Bandwidth Product	f _T	150	_	MHz	$V_{CE} = -10V$, $I_{C} = -50mA$, $f = 100MHz$

Notes: 5. Short duration pulse test used to minimize self-heating effect.

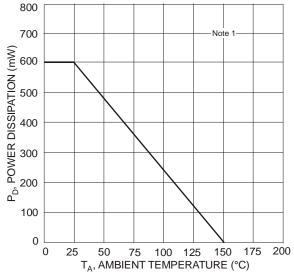


Fig. 1, Max Power Dissipation vs. Ambient Temperature

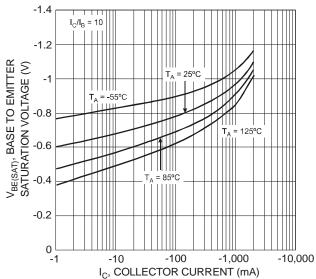


Fig. 3, Base-Emitter Saturation Voltage vs. Collector Current

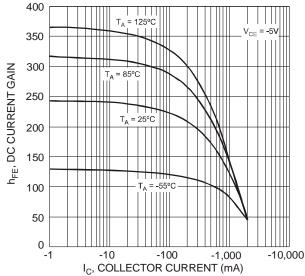


Fig. 2, DC Current Gain vs. Collector Current

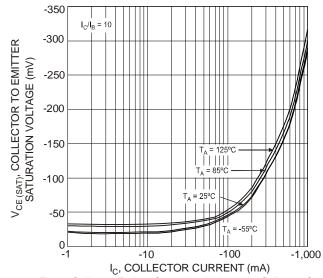


Fig. 4, Collector-Emitter Saturation Voltage vs. Collector Current

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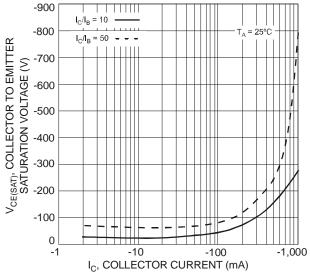
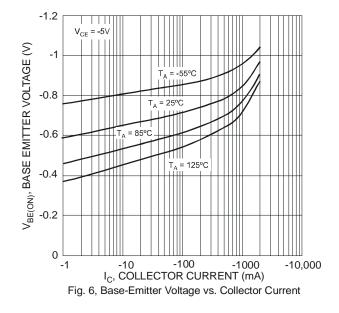


Fig. 5, Collector-Emitter Saturation Voltage vs. Collector Current



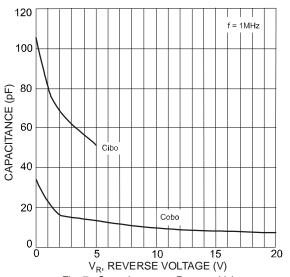


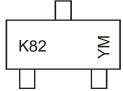
Fig. 7, Capacitance vs. Reverse Voltage

Ordering Information (Note 6)

Device	Packaging	Shipping				
DPBT8105-7	SOT-23	3000/Tape & Reel				

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



K82 = Product Type Marking Code YM = Date Code Marking Y = Year ex: S = 2005

M = Month ex: 9 = September

Date Code Key

Year	2004	20	05	2006	2007	20	800	2009	2010	20	11	2012
Code	R	,	3	T	J	'	V	W	Х	,	Y	Z
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



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